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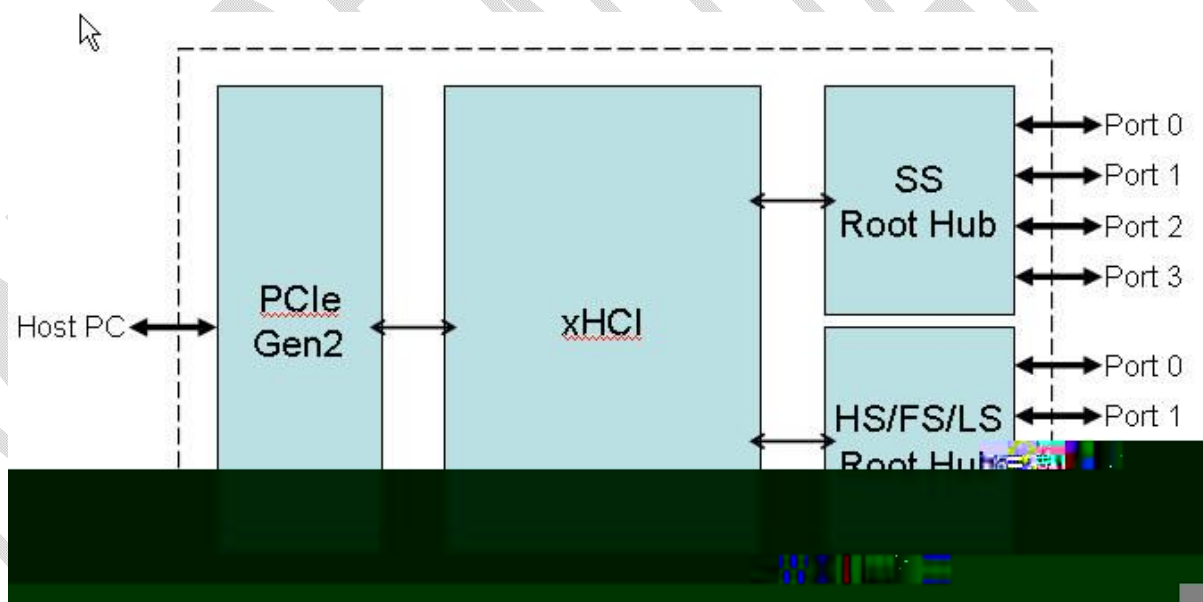
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The EJ188 is a PCIe Gen 2 to Universal Serial Bus 3.0 host controller, which complies with PCIe Rev 2.0 specification, USB3.0 specification and eXtensible Host Controller Interface (xHCI) 1.0.

The EJ188 has a PCIe Gen2 x1 lanes for the PC host system and four USB3.0 downstream ports, which provide SuperSpeed, HighSpeed, FullSpeed and LowSpeed signals for USB 3.0 compliant peripherals and existing USB 2.0/1.1 peripheral devices.



**Figure 1: EJ188 Block Diagram**



- Compliant with the PCI Express Base Specification Revision 2.0
  - Support PCIe Gen2 x1 lanes
- Supports the PCI Bus Power Management Interface Specification Revision 1.2
- Supports the SPI/I2C type ROM/flash
- System clock uses either 25MHz crystal or external clock
- 3.3V for I/O power and 1.2V for core power
  
- Compliant with the Universal Serial Bus 3.0 specification Revision 1.0
  - Supports data rate :
    - LowSpeed(1.5 Mbps)
    - FullSpeed(12 Mbps)
    - HighSpeed(480 Mbps)
    - SuperSpeed(5 Gbps)
  - Four downstream USB3.0 ports
  - Support USB data transfer types:
    - Control
    - Bulk(stream)
    - Interrupt
    - Isochronous transfer
  - Compliant with eXtensible Host Controller Interface (xHCI) Specification revision 1.0
  
- 88-QFN (10 x 10)